

SGRJ_V14_V15 / PLC_1 [CPU 1516-3 PN/DP] / Program blocks

FC907A_D(DRIVER_SPEED) [FC907]

FC907A_D(DRIVER_SPEED) Properties

General

Name	FC907A_D(DRIVER_SPEED)	Number	907	Type	FC	Language	STL
Numbering	Manual						

Information

Title		Author	ABLYY	Comment		Family	ABLYY
Version	1.0	User-defined ID					

FC907A_D(DRIVER_SPEED)

Name	Data type	Offset	Default value	Comment
▼ Input				
IN	Int			Read Bit Value
MAX_BIT	Real			Max Bit Value
MAX_ACT	Real			Max Actual Value
Output				
▼ InOut				
OUT	Real			实际检测值
▼ Temp				
Temp_DInt	DInt	0.0		
Temp_Real	Real	4.0		
Constant				
▼ Return				
FC907A_D(DRIVER_SPEED)	Void			

Network 1:

```

0001      A(
0002      L      #IN
0003      ITD
0004      T      #Temp_DInt
0005      SET
0006      SAVE
0007      CLR
0008      A BR
0009      )
0010      JNB   _001
0011      L      #Temp_DInt
0012      DTR
0013      T      #Temp_Real
0014 _001: NOP 0
    
```

Network 2:

```

0001      A(
0002      L      #Temp_Real
0003      L      #MAX_BIT
0004      /R
0005      T      #Temp_Real
0006      AN OV
0007      SAVE
0008      CLR
0009      A BR
0010      )
0011      JNB   _002
0012      L      #Temp_Real
0013      L      #MAX_ACT
0014      *R
0015      T      #OUT
0016 _002: NOP 0
    
```

Network 3:

```

0001
0002      SET
0003      SAVE
0004      CLR
0005      BE
    
```