

Terminal Assignment and Block Diagram of the SM 321; DI 32 × 24 VDC

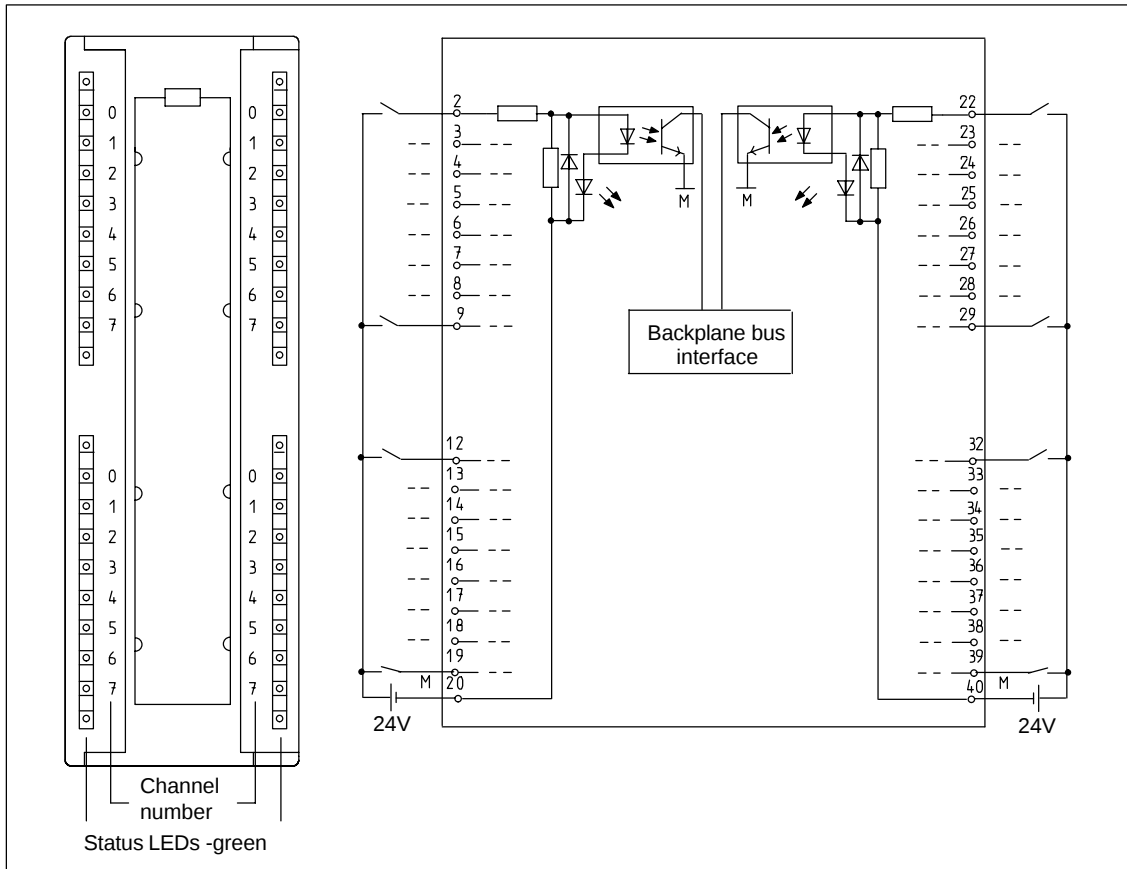


Figure 3-1 Module View and Block Diagram of the Digital Input Module SM 321; DI 32 × 24 VDC

Terminal Assignment of the SM 321; DI 32 × 24 VDC

The following figure shows the assignment of the channels to the addresses.

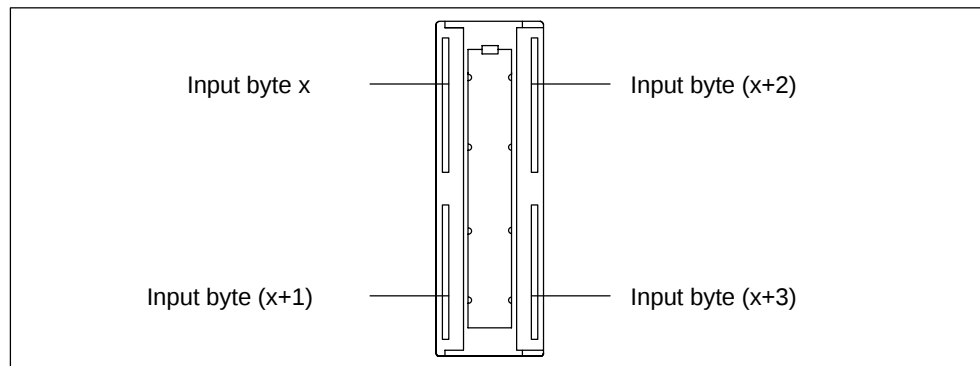


Figure 3-2 Terminal assignment of the SM 321; DI 32 × 24 VDC